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BSTZ Docket No. 042390P6447

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Michael Chow, et al.

Serial No. 09/505,949

Filed: February 15, 2000

For: **METHOD AND APPARATUS FOR  
ACHIEVING ARCHITECTURAL  
CORRECTNESS IN A MULTI-MODE  
PROCESSOR PROVIDING  
FLOATING-POINT SUPPORT**

Examiner: Li, Aimee J.

Art Unit: 2183

Confirmation No.: 5605

**RECEIVED**

**JAN 10 2003**

**Technology Center 2100**

**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Box Amendments - No Fee  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action mailed October 2, 2002, Applicants respectfully request entry of the amendments, and consideration of the remarks which follow. Attached hereto is a marked-up version of the changes made to the abstract and claims by the current amendment. The attached page is captioned "VERSION WITH MARKINGS TO SHOW CHANGES MADE".

**IN THE SPECIFICATION**

On page 1, please amend the paragraph beginning on line 10 as follows:

Processors have become ubiquitous in modern society. Processors are found in many popularly used electronic devices such as, for example, personal computers, personal digital assistants, and cellular phones. Processors are also used in devices not thought of as traditionally being electronics, for example, automobiles and coffee makers. Processors used in today's most popular computers include software typically referred to as microcode. Microcode within a processor is implemented to achieve a defined set of assembly language instructions which are executed by the processor